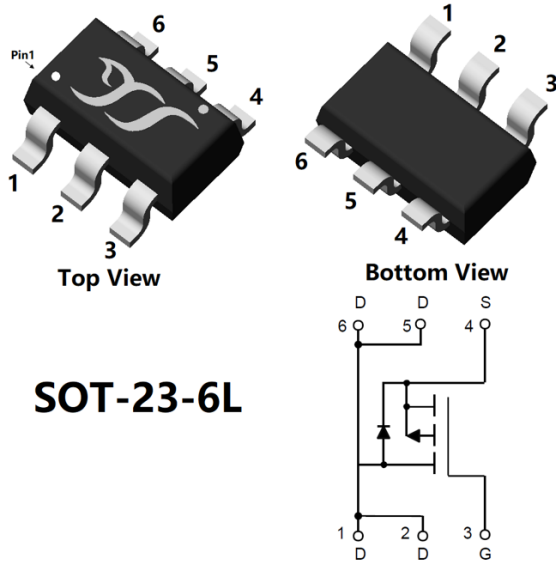


P-Channel Enhancement Mode Field Effect Transistor



Product Summary

• V_{DS}	-60V
• I_D	-2.8A
• $R_{DS(ON)}$ (at $V_{GS}=-10V$)	<100m Ω
• $R_{DS(ON)}$ (at $V_{GS}=-4.5V$)	<120m Ω

General Description

- High density cell design for Low $R_{DS(ON)}$
- High Speed switching
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free

Applications

- PWM applications
- Power management
- Load switch

Limiting Values

Parameter	Conditions	Symbol	Min	Max	Unit	
Drain-source Voltage	$T_J \geq 25^\circ C; T_J \leq 150^\circ C$	V_{DS}	-	-60	V	
Gate-source Voltage	$T_J \leq 150^\circ C; DC$	V_{GS}	-20	20		
Continuous Drain Current (Note 1,2)	Steady-State	I_D	$T_A=25^\circ C, V_{GS}=-10V$	-	-2.8	A
			$T_A=100^\circ C, V_{GS}=-10V$	-	-1.8	
Pulsed Drain Current	$T_A=25^\circ C, t_p \leq 10\mu s$	I_{DM}	-	-22.4		
Maximum Body-Diode Continuous Current	$T_A=25^\circ C$	I_S		-1.6		
Total Power Dissipation (Note 1,2)	Steady-State	P_D	$T_A=25^\circ C$	-	1.5	W
			$T_A=100^\circ C$	-	0.6	
Junction and Storage Temperature Range		T_J, T_{STG}	-55	150	$^\circ C$	

Thermal Resistance

Parameter	Symbol	Typ	Max	Units
Thermal Resistance Junction-to-Ambient (Note 2)	$R_{\theta JA}$	-	84	$^\circ C/W$

Ordering Information (Example)

PREFERRED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
YJJ100P06AJ	F2	100AJ	3000	30000	120000	7" reel



YJJ100P06AJ

■ Electrical Characteristics

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=-250\mu A, T_j=25^\circ C$	-60	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-60V, V_{GS}=0V, T_j=25^\circ C$	-	-	-1	μA
		$V_{DS}=-60V, V_{GS}=0V, T_j=150^\circ C$	-	-	-100	
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V, T_j=25^\circ C$	-	-	± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A, T_j=25^\circ C$	-1.2	-1.7	-2.2	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=-10V, I_D=-2A, T_j=25^\circ C$	-	75	100	$m\Omega$
		$V_{GS}=-4.5V, I_D=-1A, T_j=25^\circ C$	-	93	120	$m\Omega$
Diode Forward Voltage	V_{SD}	$I_S=-2A, V_{GS}=0V, T_j=25^\circ C$	-	-0.82	-1.2	V
Gate Resistance	R_G	$f=1MHz, T_j=25^\circ C$	-	5.8	-	Ω
Dynamic Parameters						
Input Capacitance	C_{iss}	$V_{DS}=-30V, V_{GS}=0V, f=1MHz, T_j=25^\circ C$	-	900	-	pF
Output Capacitance	C_{oss}		-	44	-	
Reverse Transfer Capacitance	C_{rss}		-	34	-	
Switching Parameters						
Total Gate Charge	Q_g	$V_{GS}=-10V, V_{DS}=-30V, I_D=-2A, T_j=25^\circ C$	-	18	-	nC
Gate-Source Charge	Q_{gs}		-	2.4	-	
Gate-Drain Charge	Q_{gd}		-	2.1	-	
Reverse Recovery Charge	Q_{rr}	$I_F=-2A, di/dt=100A/\mu s, V_{GS}=0V, V_R=-30V, T_j=25^\circ C$	-	16	-	nC
Reverse Recovery Time	t_{rr}		-	17	-	ns
Turn-on Delay Time	$t_{D(on)}$	$V_{GS}=-10V, V_{DS}=-30V, I_D=-2A, R_L=15\Omega, R_{GEN}=3\Omega, T_j=25^\circ C$	-	6.7	-	ns
Turn-on Rise Time	t_r		-	3	-	
Turn-off Delay Time	$t_{D(off)}$		-	28	-	
Turn-off Fall Time	t_f		-	5.6	-	

Note:

- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- The value of $R_{\theta JA}$ is measured with the device mounted on the 40mm*40mm*1.1mm single layer FR-4 PCB board with 1 in² pad of 2oz. Copper, in the still air environment with $T_A=25^\circ C$. The maximum allowed junction temperature of 150 $^\circ C$. The value in any given application depends on the user's specific board design.
- Thermal resistance from junction to soldering point (on the exposed drain pad)



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Typical Electrical and Thermal Characteristics Diagrams

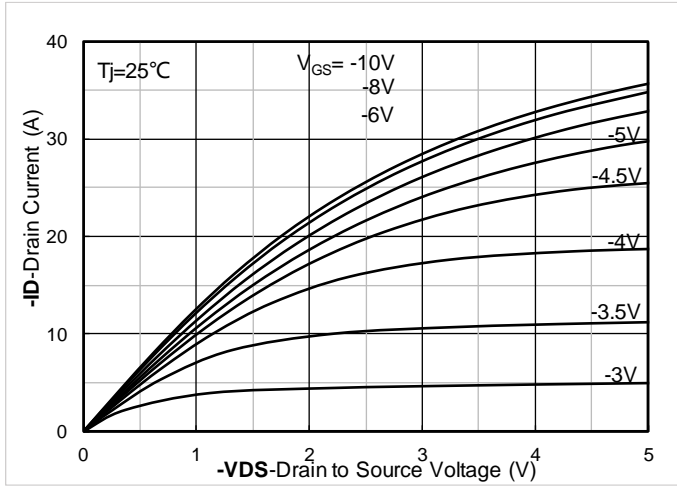


Figure 1. Output Characteristics; typical values

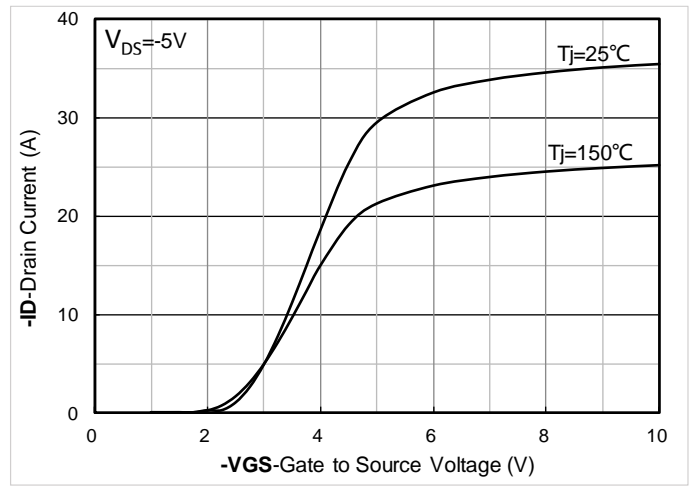


Figure 2. Transfer Characteristics; typical values

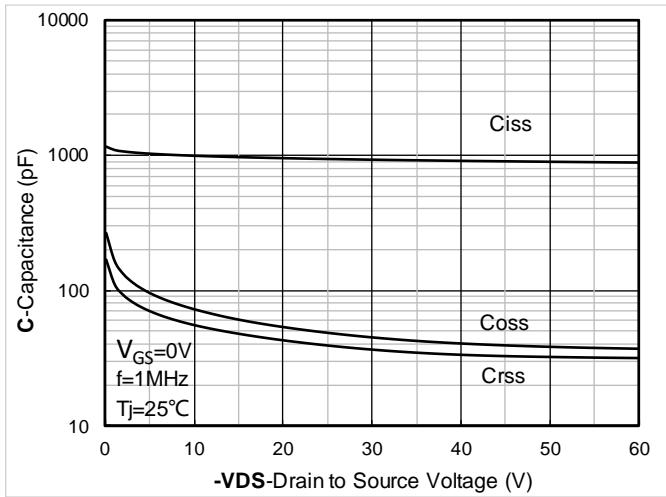


Figure 3. Capacitance Characteristics; typical values

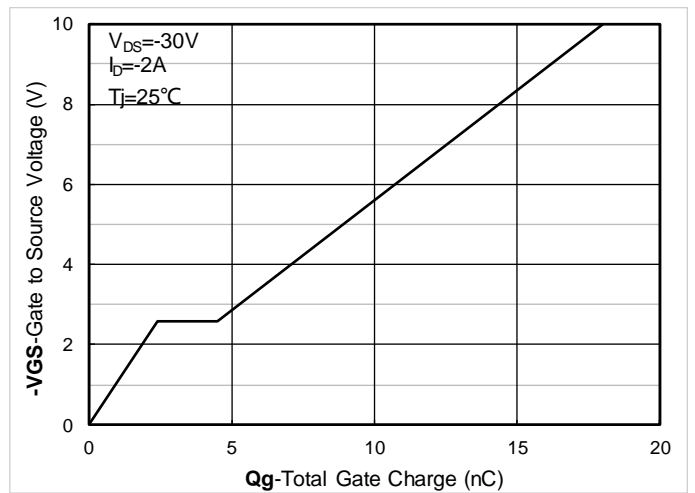


Figure 4. Gate Charge; typical values

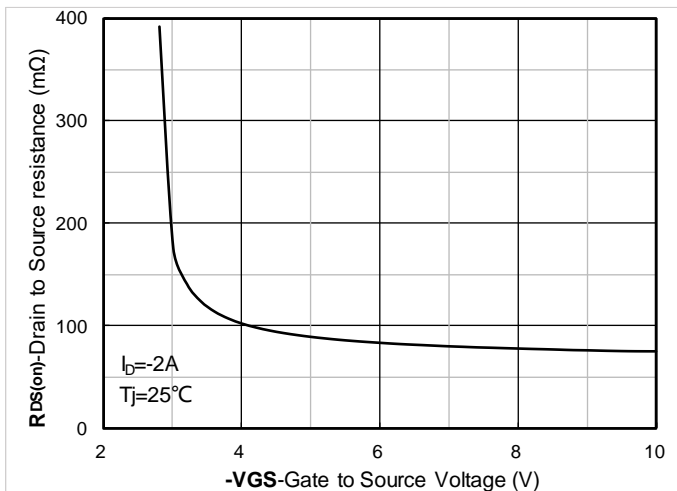


Figure 5. On-Resistance vs Gate to Source Voltage; typical values

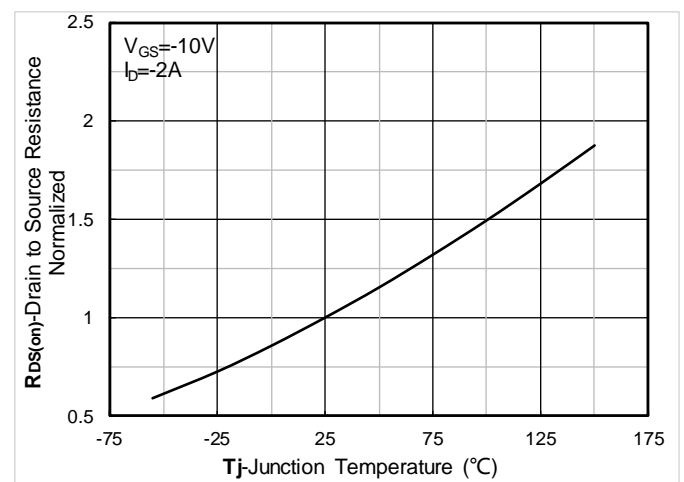


Figure 6. Normalized On-Resistance



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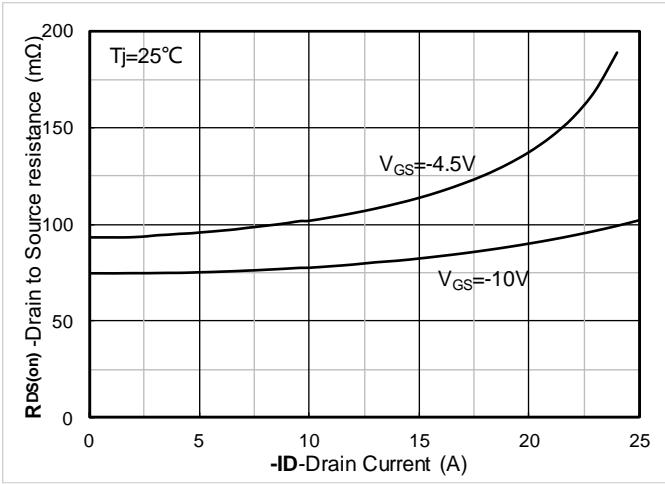


Figure 7. RDS(on) VS Drain Current; typical values

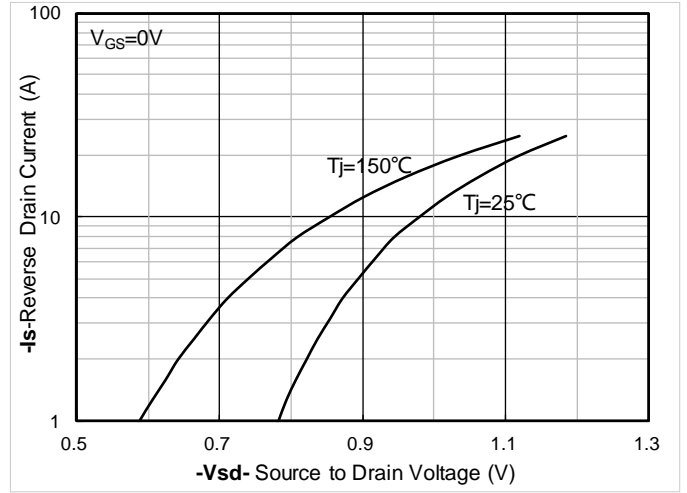


Figure 8. Forward characteristics of reverse diode; typical values

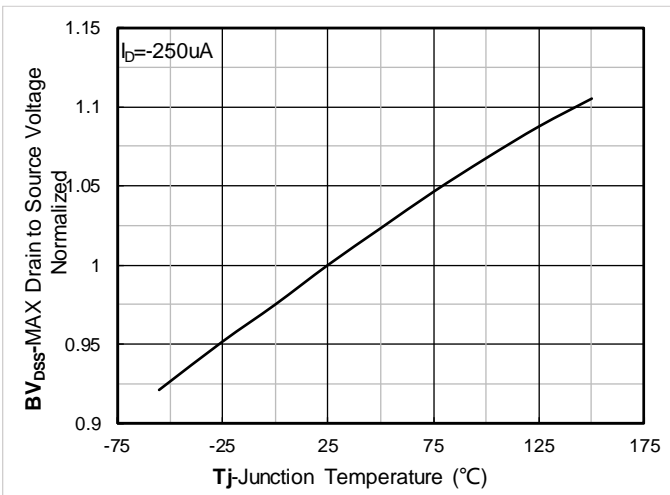


Figure 9. Normalized breakdown voltage

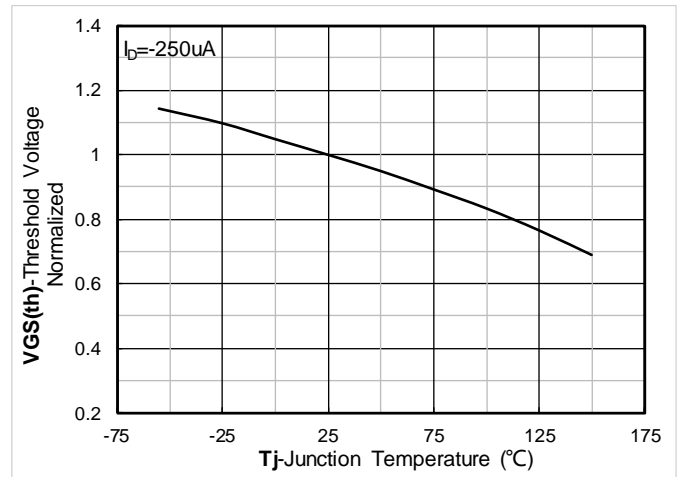


Figure 10. Normalized Threshold voltage

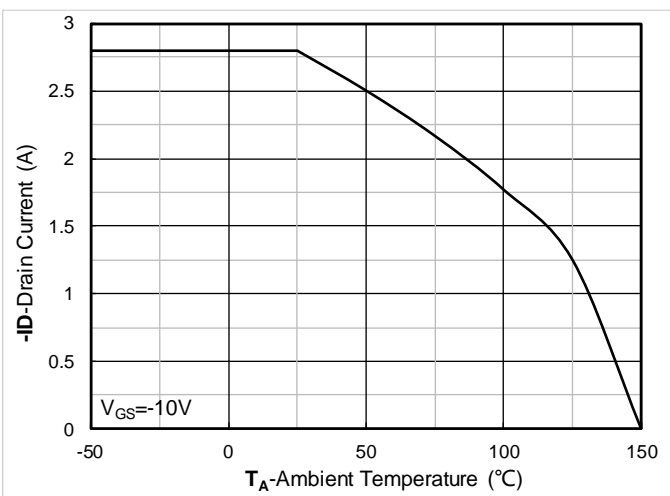


Figure 11. Current dissipation

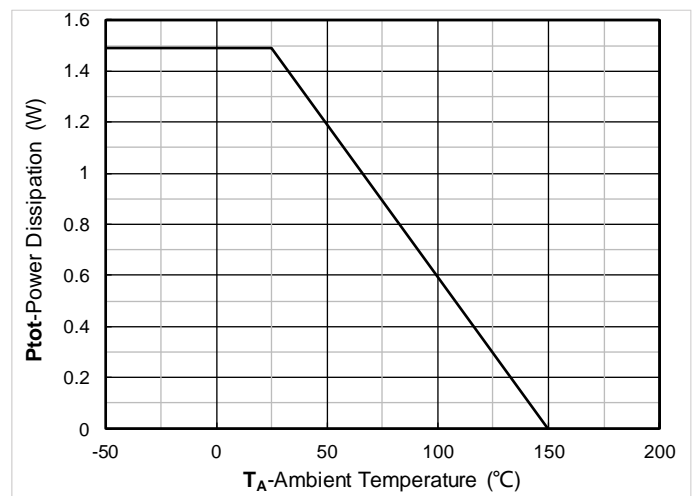


Figure 12. Power dissipation



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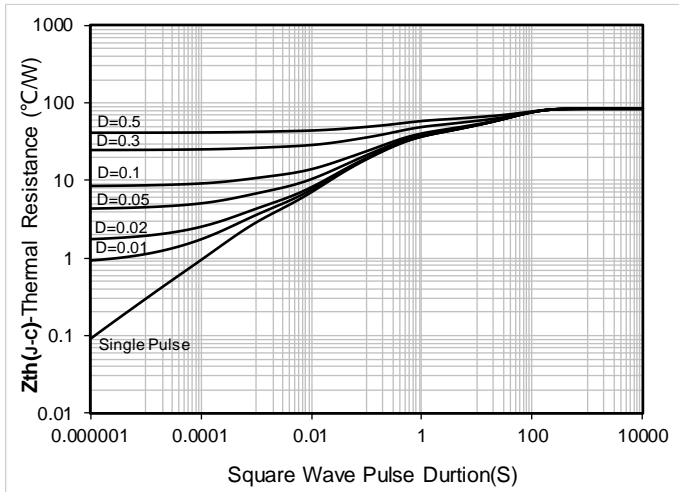


Figure 13. Maximum Transient Thermal Impedance

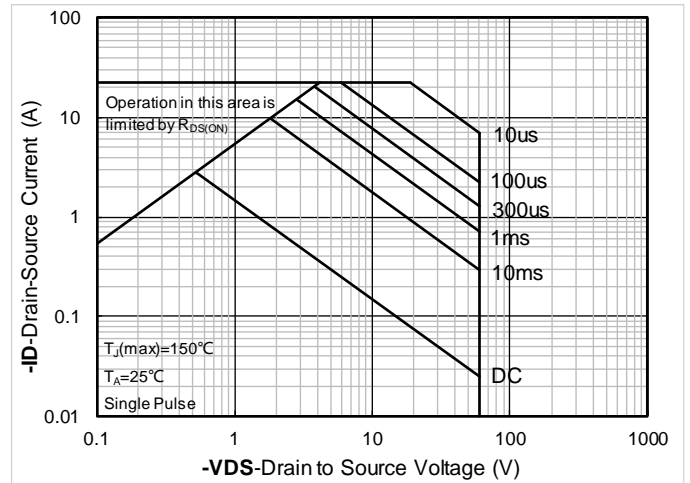
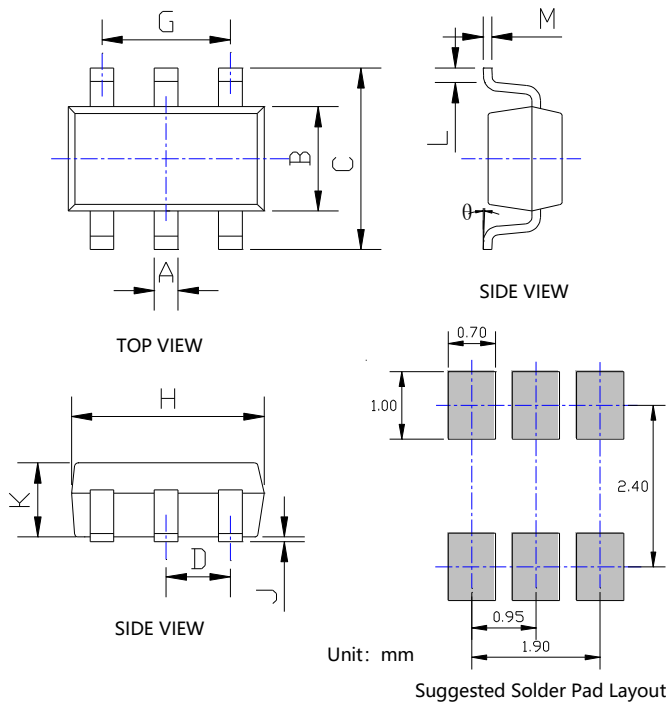


Figure 14. Safe Operation Area



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■ SOT-23-6L Package information



SYMBOL	DIMENSIONS			
	INCHES		Millimeter	
	MIN.	MAX.	MIN.	MAX.
A	0.012	0.020	0.300	0.500
B	0.059	0.067	1.500	1.700
C	0.104	0.116	2.650	2.950
D	0.037BSC		0.950BSC	
G	0.075BSC		1.900BSC	
H	0.111	0.119	2.820	3.020
J	0.000	0.004	0.000	0.100
K	0.041	0.045	1.050	1.150
L	0.012	0.024	0.300	0.600
M	0.004	0.008	0.100	0.200
θ	0°	8°	0°	8°

Note:

1. Controlling dimension: in millimeters.
2. General tolerance: ± 0.05 mm.
3. The pad layout is for reference purposes only.



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